

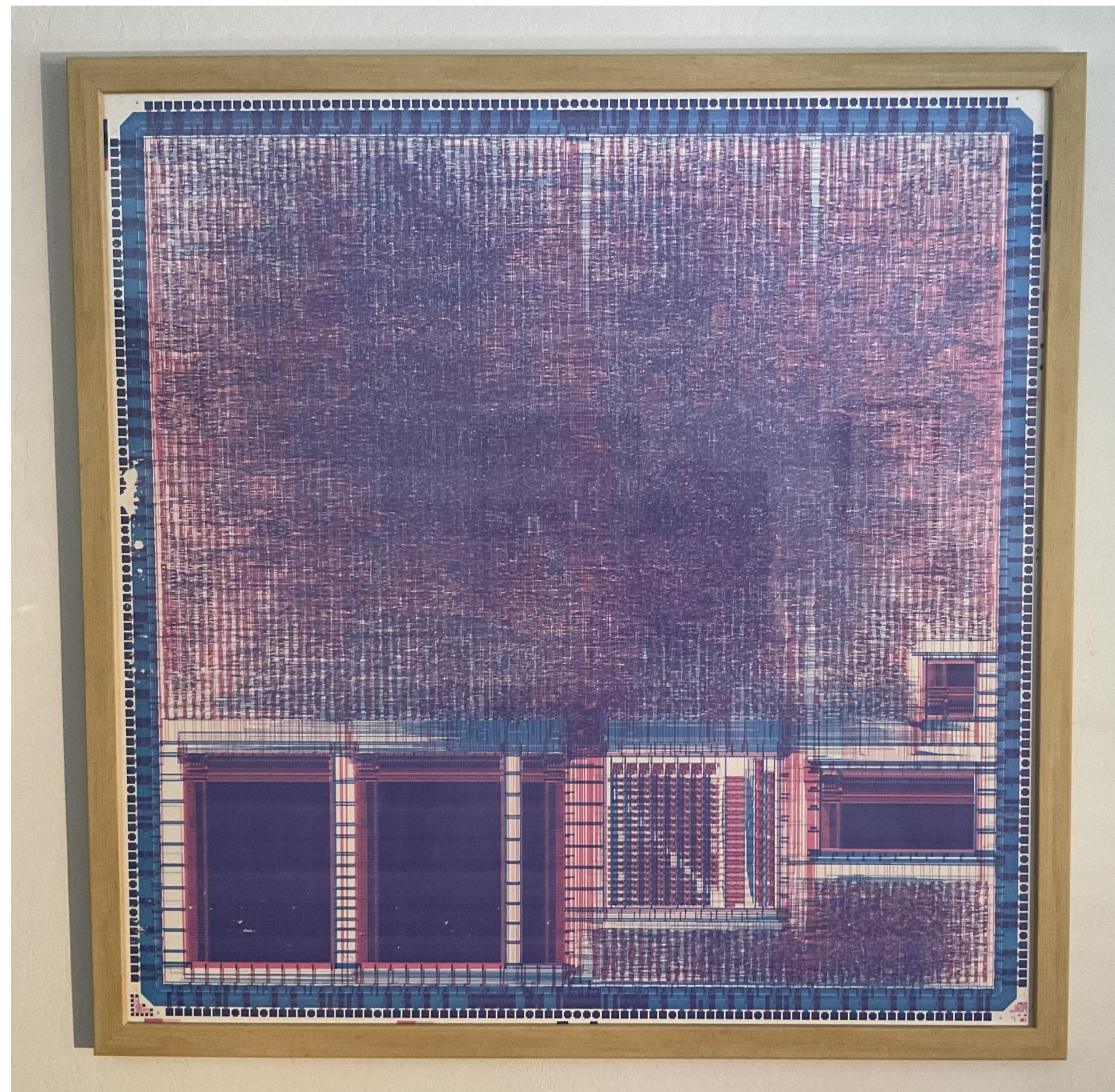
Titan Graphics Supercomputer: G3 Pixel Processor

Advanced ASIC Routing

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¹LSI Logic, ²Ardent Computer

Wiring Plot



Team

DATE : Sep 27, 1989
FROM : PRADEEP VAJRAM
TO : LIST
SUBJECT : ARDENT PROJECT'S STATUS

OVERVIEW : The major topic of the last two weeks is routing of the GPIC_IO. We currently have two separate account A and B working in parallel.

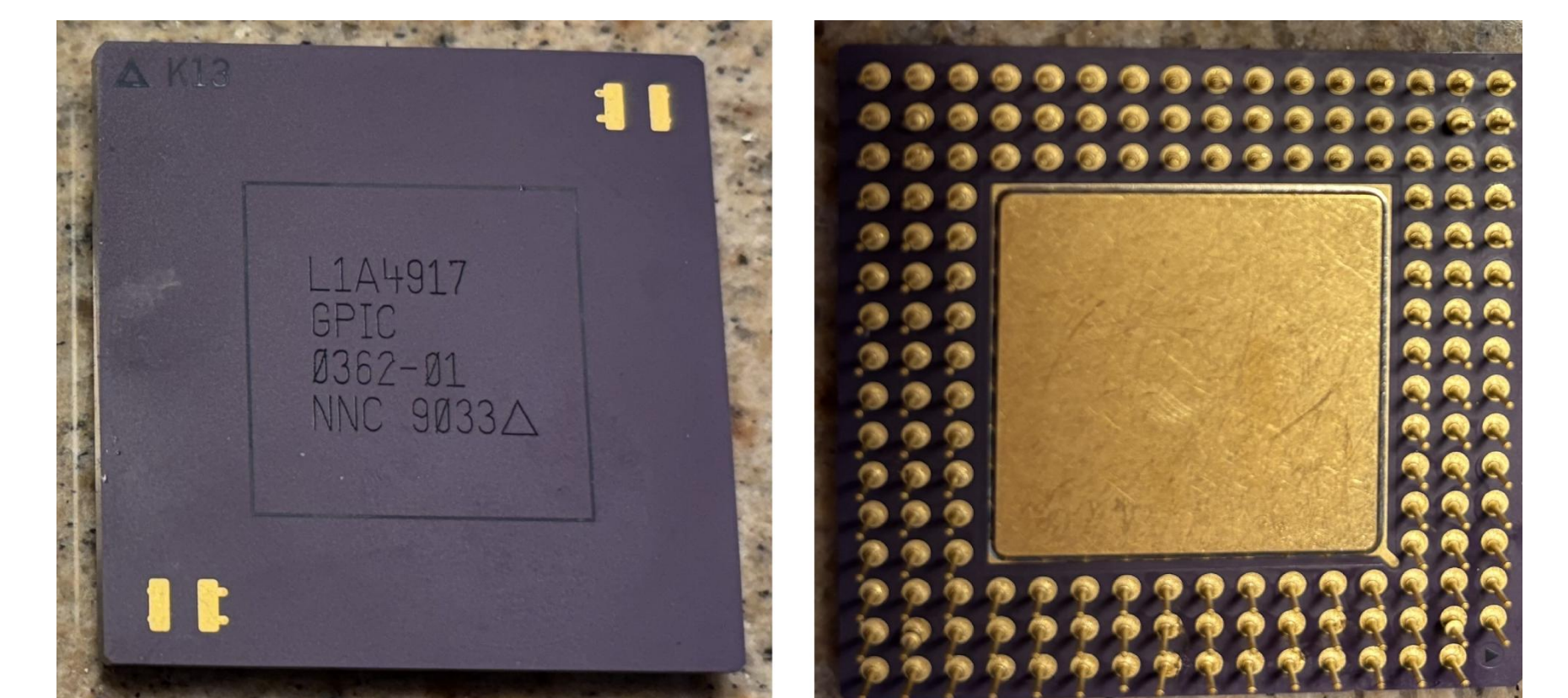
Layout A : After the design change removed (58) nets) we had 34 open nets after the final routing program. The layout dept. has started to route the rest of the open nets manually. We have completed about 8 nets since 09/26/89. (16)

Layout B :
In this account we merged the new design with the old design file which has only clocks and power routed. Currently we are running the first auto routing program.

Auto Router (CUR1) : 2 day's
Clean wire (CW) : one day
Auto Router (CUR2) : one day

GRIO_IO : Running the final routing program. Expecting the seglén file tomorrow.

List :
1) Jen-Hsun Huang
2) Tom Diede
3) Avi Bleiweiss
4) Zaigham Ahsan



Physical GPIC
(156 pins)



Celebrating an outstanding
system bring-up
(Gordon Bell and Avi Bleiweiss)

Courtesy of LSI Logic, 1.5 Micron technology
September 1989